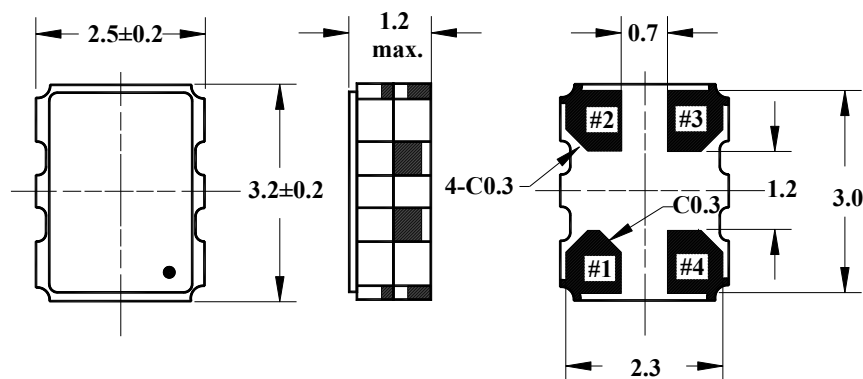


Frequency	32.768 KHz
Output Level	HC-MOS
Level "0"	VOL: 0.4V Max (CL 15pF at 25°C)
Level "1"	VOH: VDD-0.4V min (CL 15pF at 25°C)
Duty Cycle	50% ± 10% (@ 50% V Level)
Rise/ Fall Time	200 nS Max.
Output Load	15 pF ± 5% (CMOS load)
Enable / Disable	#1: 90% VDD min. (ViH) / #3: Output / #1: 10% VDD max. (ViL) / #3 High Impedance
Disable Delay Time	100 nS max
Enable Delay Time	1 sec max
Overall Frequency Stability	+30ppm to -90ppm over -20 to +70°C / +30ppm to -130ppm over -40 to +85°C
Supply Voltage	See Table Below
Input Voltage (VDD)	+1.5V to +5.5V
Absolute Supply Voltage	+1.5V to +5.5V
Input Current	1.5uA max (VDD = +3.3V without load over -40 to +85°C)
Stand-By Current (Pin 1-VIL)	250nA max (without load at 25°C)
Temperature	
Operating	-40 to +85°C
Storage	-55 to +125°C
Aging	±5ppm max at 25°C ±3°C for first year

Part Number:		QCDO-3ATA1T-32.768KHz					
	Supply Voltage	Operating Temp	Symmetry (Duty Cycle)	Output	Stability (in ppm)	Tristate (Standby)	Frequency (KHz)
QCDO	3 = 3.3V	A = 0°C ~ +70°C	T = 45/55 max	A = HC-MOS	1 = ±100	Tristate (Standby)	32.768
	2 = 2.5V	B = -10°C ~ +70°C	S = 40/60 max		2 = ±50		
	1 = 1.8V	C = -20°C ~ +70°C			3 = ±25		
		E = -40°C ~ +85°C					

Dimensions: (Unit=mm)



PIN CONNECTIONS

- 1 "L" / "H"
- 2 GND
- 3 Z /
- 4 Output
- VDD

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Specifications subject to change without notice (Rev B)