

QCV35 Series

SMD VCXO, 14 mm x 10 mm, PECL / LVDS

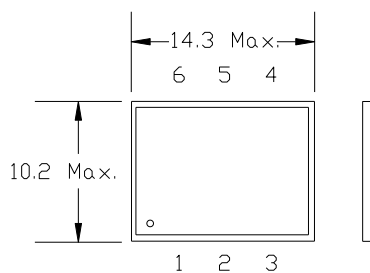
Frequency	1.000 MHz to 750.000 MHz	
Output Level	LVDS	PECL
Level	Vod = 393 mV Typ., 475 mV Max.	'0' = Vcc - 1.63 VDC Max. '1' = Vcc - 1.02 VDC Min.
Duty Cycle	50% ± 10%	
Rise / Fall Time	0.6 nS Max.	
Output Load	100 Ω Differential	50 Ω to Vcc - 2.0 VDC
Frequency Stability	±50 ppm Max.	
Supply Voltage	3.3 VDC ±10%	
Current	90 mA Max.	130 mA Max.
Control Voltage	1.65 VDC ± 1.5 VDC	
Linearity	±10%	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental	See Appendix B for information	
Package Information	MSL = 2a, Termination = e2 or e4	

Part Number Guide		Sample Part Number: QCV35 - A21AT - 250.000				
Package	Operating Temperature	Frequency Stability	Pullability	Output	Enable / Disable	Frequency
QCV35 -	A = 0° C to +70° C	1 = ±100 ppm	1 = ± 100 ppm Min.	A = LVDS	T = Enable	- 250.000 MHz
	C = -20° C to +70° C	2 = ±50 ppm	2 = ± 50 ppm Min.	B = PECL		
	E = -40° C to +85° C	3 = ±25 ppm				
		4 = ±20 ppm				

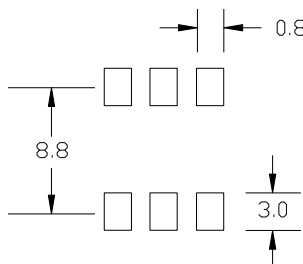
NOTE: A 0.01 μF bypass capacitor is recommended between Vcc (pin 6) and GND (pin 3) to minimize power supply noise.

- Not available for all temperature ranges.

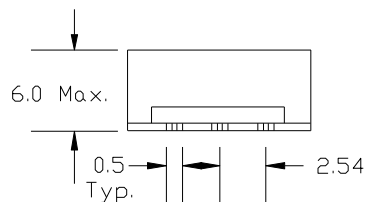
Dimension Units: mm



Recommended pad layout



Tri-State Function	
Pin 2 Open	Enable
Pin 2 ≥ 70% Vdd	Enable
Pin 2 ≤ 30% Vdd	Disable



Pin	Connection
1	Voltage Control
2	Tristate
3	GND
4	Output
5	Comp. Output
6	Vcc

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Specifications subject to change without notice (Rev B)