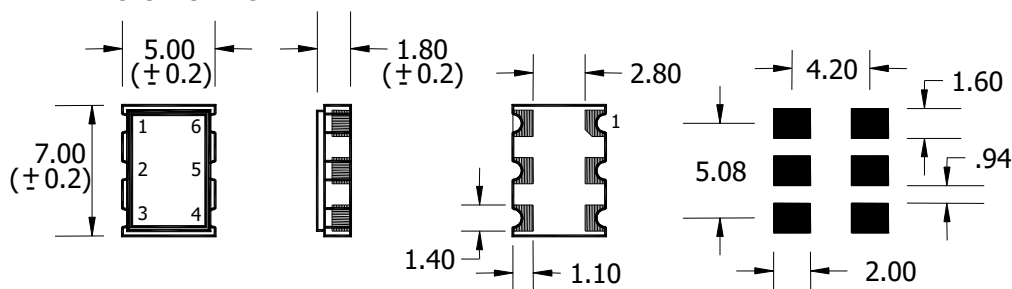


Frequency	1.0 MHz to 750.0 MHz	
Output Level	LVDS	PECL
Level	Vod = 393 mV Typ., 475 mV Max	'0' = Vcc - 1.63 VDC Max '1' = Vcc - 1.02V Min
Duty Cycle	50% ± 10%	
Rise/ Fall Time	0.6 nS Max	
Output Load	100 Ω Differential	50 Ω to Vcc - 2.0 VDC
Stability		
Frequency vs. Temperature	See table below	
Aging @ 25°C	± 5.0 ppm/year Max	
Control Voltage	1.65 VDC ± 1.5 VDC	
Voltage Control Range	See table below	
Slope	Positive	
Supply Voltage	3.3 Vdc ± 10%	
Current	90 mA Max	130 mA Max
Temperature		
Operating	See table below	
Storage	-55°C to +125°C	
Environmental	See Appendix B for information	
Package Information	MSL = N.A., Termination = e4	

**Part Number Guide**      **Sample Part Number:**      **QCV40-A22AT-200.000**

	Operating Temperature	Frequency Stability	Pullability	Output	Enable/Disable	Frequency
QCV40-	A = 0 to +70 C	1 = ± 100ppm max	1 = ± 100ppm min	A = LVDS	T = Enable	-200.000MHz
	C = -20 to +70 C	2 = ± 50ppm max	2 = ± 50ppm min	B = PECL		
	E = -40 to +84 C	3 = ± 25ppm max				
		5 = ± 15ppm max				

DIMENSION UNITS: mm



- PIN CONNECTIONS**
- 1 CONTROL VOLTAGE
  - 2 ENABLE/DISABLE
  - 3 GROUND
  - 4 OUTPUT
  - 5 C. OUTPUT
  - 6 Vdd

Tri-State Function	
Pin 2 Open	Enable
Pin 2 ≥ 70% Vdd	Enable
Pin 2 ≤ 30% Vdd	Disable

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Specifications subject to change without notice (Rev A)