

QCV60 Series

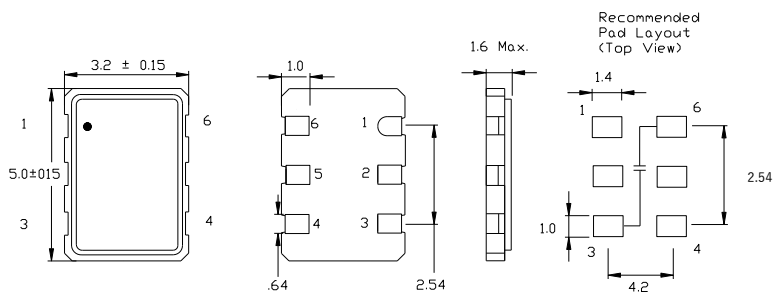
SMD VCXO, 5x3.2mm, TTL/HCMOS

Frequency	1.000 MHz to 77.760 MHz	
Output Level	TTL	HC-MOS
Level	'0' = 0.4 VDC Max., '1' = 2.4 VDC Min.	'0' = 0.1 Vcc Max., '1' = 0.9 Vcc Min.
Duty Cycle	50% ± 5%	
Rise / Fall Time	10 nS Max.	
Output Load	15 pF, Fo < 50 MHz = 10 TTL, Fo > 50 MHz = 5 LSTTL	
Frequency Stability	±50 ppm Max.	
Supply Voltage	See Input Voltage Table, tolerance ±5 %	
Current	50 mA Max. **	
Control Voltage	1.65 VDC ± 1.5 VDC for Vcc = 3.3 VDC, 2.5 VDC ± 2.0 VDC for Vcc = 5.0 VDC	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental	See Appendix B for information	
Package Information	MSL = N.A., Termination = e4	

Part Number Guide		Sample Part Number: QCV60 – A223T – 20.000				
Package	Operating Temperature	Frequency Stability	Pullability	Supply Voltage	Enable / Disable	Frequency
QCV60-	A = 0° C to +70° C	2 = ±50 ppm	1 = ±100 ppm Min	2 = 2.5 VDC	T = Yes	- 20.000 MHz
	B = -20° C to +70° C	3 = ±25 ppm	2 = ±50 ppm Min	3 = 3.3 VDC		
	C = -40° C to +85° C	4 = ±20 ppm	3 = ±150 ppm Min	5 = 5.0 VDC		

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 6) and GND (pin 3) to minimize power supply noise

* Not available for all temperature ranges. ** Frequency, supply, and load related parameters.



Pin	Connection
1	Control Voltage
2	Enable / Disable
3	Ground
4	Output
5	N.C.
6	Vdd

Bypass = 0.01 µF

Dimension Units: mm

Tri-State Function	
Pin 2 Open	Enable
Pin 2 ≥ 70% Vdd	Enable
Pin 2 ≤ 30% Vdd	Disable

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Specifications subject to change without notice (Rev D).