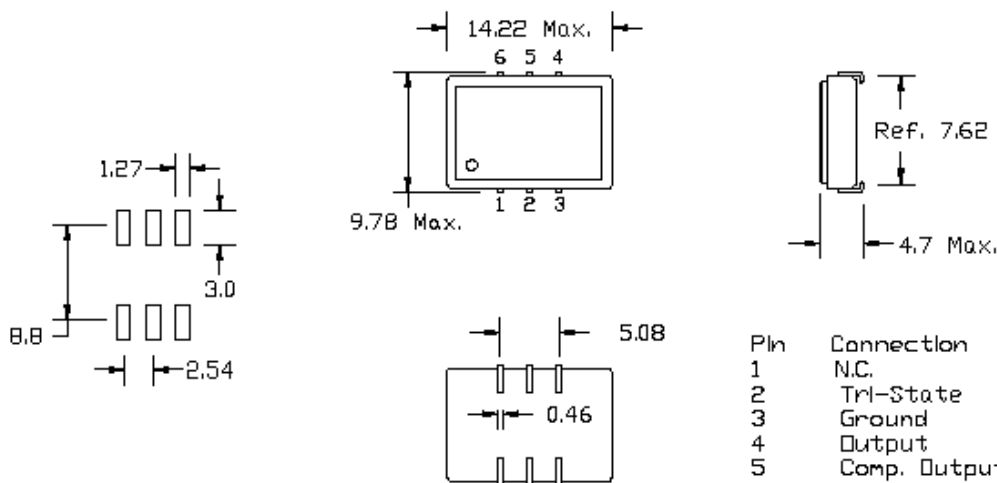


<b>Frequency</b>	1.000 MHz to 750.000 MHz						
<b>Output Level</b>	LVDS	PECL					
<b>Level</b>	Vod = 393 mV Typ., 475 mV Max.	'0' = Vcc-1.63 VDC '1' = Vcc - 1.02 VDC Min. Max.					
<b>Duty Cycle</b>	Specify 50% ± 10% or ± 5% See Table						
<b>Rise / Fall Time</b>	0.6 nS Max.						
<b>Output Load</b>	100 Ω Differential	50 Ω to Vcc - 2.0 VDC					
<b>Frequency Stability</b>	See Frequency Stability Table (Includes room temperature tolerance and stability over operating temperature)						
<b>Start-up Time</b>	10 mS Max.						
<b>Enable / Disable Time</b>	100 nS Max.						
<b>Supply Voltage</b>	3.3 VDC ±10%						
<b>Current</b>	130 mA Max. **						
<b>Temperature</b>							
<b>Operating</b>	See Operating Temperature Table						
<b>Storage</b>	-55° C to +125° C						
<b>Environmental / Tape and Reel</b>	See Application notes for Environmental information for Tape and Reel information						
<b>Package Information</b>	MSL = N.A., Termination = e4						
<b>Part Number Guide</b>	<b>Sample Part Number:</b>	<b>QJO-3ATA1T-156.250 MHz</b>					
<b>Package</b>	<b>Input Voltage</b>	<b>Operating Temperature</b>	<b>Symmetry (Duty Cycle)</b>	<b>Output</b>	<b>Stability (in ppm)</b>	<b>Enable / Disable</b>	<b>Frequency</b>
QJO-	3 = 3.3 V	A = 0° C to +70° C	T = 45 / 55 Max.	A = LVDS	1 = ± 100	H = Enable	- 156.250 MHz
		B = -10° C to +70° C	S = 40 / 60 Max.	B = PECL	2 = ± 50		
		C = -20° C to +70° C			3 = ± 25		
		D = -30° C to +75° C			4 = ± 20		
		E = -40° C to +85° C			*5 = ± 15		

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 6) and GND (pin 3) to minimize power supply noise.

\* Not available for all temperature ranges.

\*\* Frequency, supply, and load related parameters.



Pin	Connection
1	N.C.
2	Tri-State
3	Ground
4	Output
5	Comp. Output
6	Vcc

Tri-State Function	
Pin 2 Open	Enable
Pin 2 70% Vdd	Enable
Pin 2 30% Vdd	Disable

Dimension Units: mm

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Specifications subject to change without notice (Rev A)