QPG Series

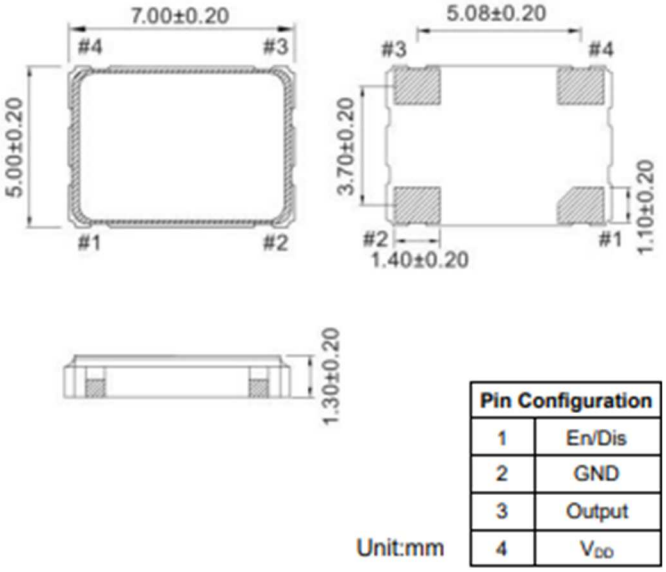
SMD Programmable Oscillator, 7.0 x 5.0mm, HCMOS

RoHS Compliant

Supply Voltage (V_{DD})		1.8V	2.5V		3.3V
Frequency		1 MHz to 125 MHz	1 MHz to 200 MHz		1 MHz to 200 MHz
Output Voltage Logic			Logic "1" = 90% V_{DD} Min Logic "0" = 10% V_{DD} Max		
Duty Cycle			Standard 50 ±10%, Option 50 ±5% (See Table in Part Number Guide)		
Load Drive Capability			15pF		
Frequency Stability			See Frequency Stability Table in Part Number Guide		
Level Logic '1' (V_{OH})			0.9 x V_{DD} min		
Level Logic '0' (V_{OL})			0.1 x V_{DD} max		
Rise / Fall Time		3.0 nSec max	2.0 nSec max		2.0 nSec max
Start-Up Time			8.0 mSec max		
Tristate Voltage	Enable		0.7 x V_{DD} ≤ V_{IH} ≤ V_{DD}		
	Disable		V_{IL} ≤ 0.3 x V_{DD}		
Supply Current		20 mA max	30 mA max		35 mA max
Aging			± 3.0 ppm max @ +25ºC Max First Year		
Temperature Range					
Operating			See Operating Temperature Table in Part Number Guide		
Storage			-55ºC to +125ºC		
RMS Phase Jitter(12kHz to 20MHz)			2.0 pSec max		

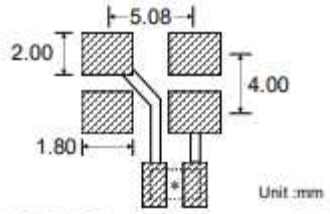
Part Number Guide **Sample Part Number: QPG1-3ET3T-20.000**

Package	Supply Voltage	Operating Temperature	Symmetry (Duty Cycle)	Stability (in ppm)	Function	Frequency (in MHz)
QPG	1 = +1.8V	A = 0ºC to +70ºC	T = 45/55 max	1 = ±100	T = Tristate	20.000
	2 = +2.5V	C = -20ºC to +70ºC	S = 40/60 max	2 = ±50		
	3 = +3.3V	E = -40ºC to +85ºC		3 = ±25		
				4 = ±15		
				5 = ±10		



Pin Configuration	
1	En/Dis
2	GND
3	Output
4	V_{DD}

Solder Pattern



*0.01µF to 0.1µF bypass capacitor recommended

QVS TECH INC
6965 El Camino Real, Ste 105 Carlsbad, CA 92009 Phone: 760-929-8677 Fax: 760-929-8077
email: sales@qvstech.com

Specifications subject to change without notice.