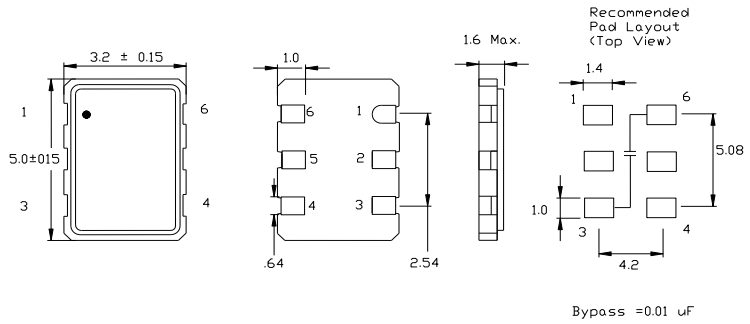
QRO Series

SMD Oscillator, 5x3.2mm, LVDS or LVPECL

Frequency	80 MHz to 270 MHz	25 MHz to 350 MHz
Output Level	LVDS	LVPECL
Level – Vdd 2.5V	VOH = 1.43 typ. VOL = 1.10 typ.	VOH = 1.475 Min. VOL = 1.095 Max.
Level – Vdd 3.3V	VOH = 1.43 typ. VOL = 1.10 typ.	VOH = 2.275 Min. VOL = 1.680 Max.
Duty Cycle	50% ± 5%	
Rise / Fall Time	0.7 nS Max.	
Output Load	100 Ω (offset 1.25V typ.)	50 Ω to Vcc - 2.0 VDC
Frequency Stability	See Frequency Stability Table	
Supply Voltage	See Supply Voltage Table (Tolerance ±5 %)	
Current	55 mA Typ.	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Package Information	MSL = N.A., Termination = e4	

Part Number Guide	**Sample Part Number:**	**QRO - 3AA1T - 100.000**				
Package	**Supply Voltage**	**Operating Temperature**	**Output**	**Frequency Stability**	**Enable / Disable**	**Frequency**
QRO-	2 = 2.5 VDC	A = 0° C to +70° C	A = LVDS	1 = ±100 ppm	T = Yes	-100.000 MHz
	3 = 3.3 VDC	C = -20° C to +70° C	B = LVPECL	2 = ±50 ppm		
		E = -40° C to +85° C		3 = ±25 ppm		



Pin Connection	
Pin 1	Enable
Pin 2	N.C.
Pin 3	Vss
Pin 4	Output
Pin 5	Comp Output
Pin 6	Vdd

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