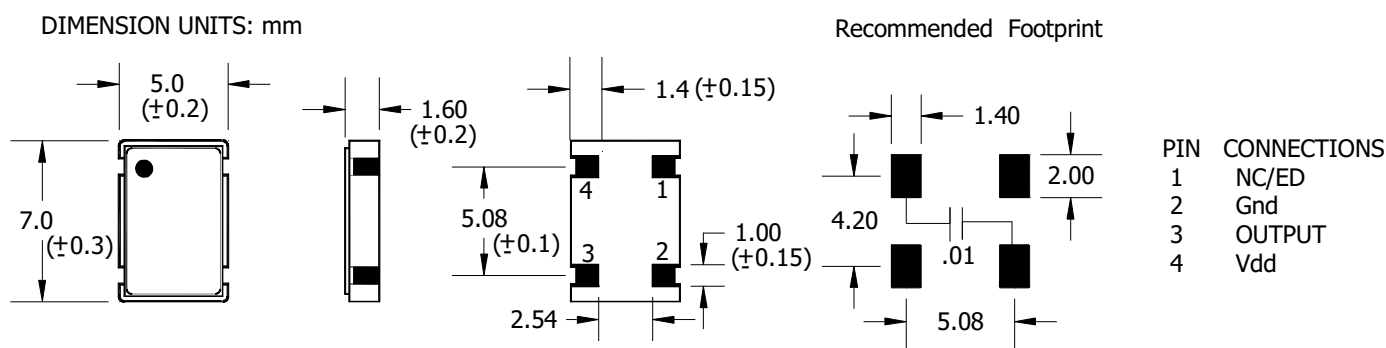


Frequency	32.768KHz
Frequency Tolerance	± 20ppm
Supply Voltage (Vdd)	+1.2VDC to +5.5VDC
Supply Current (Vdd = +3.0VDC)	0.30 μA typ / 0.50 μA max
Output	CMOS
Output Current (I <sub>OH</sub> )	+1mA
Output Current (I <sub>OL</sub> )	-1mA
Duty Cycle	40% / 60%
Logic "1" (V <sub>OH</sub> )	V <sub>DD</sub> - 0.40VDC min
Logic "0" (V <sub>OL</sub> )	GND + 0.40VDC max
Rise / Fall Time	70 nSec max
Start Up Time	0.5 Sec max
Enable / Disable - Enable (High)	80% V <sub>DD</sub> min
Enable / Disable - Enable (Low)	20% V <sub>DD</sub> max
Voltage Coefficient	± 1.5ppm/V max
Aging first year max at 25°C	±3.0ppm
Turnover Temperature	+25°C ± 5°C typ
Frequency vs. Temperature	-0.35ppm/°C <sup>2</sup> (T-T <sub>0</sub> ) <sup>2</sup> ±10% ppm
Storage Temperature	-55°C to +125°C
Operating Temperature	-40°C to +85°C

Part Number Guide		Sample Part #:		QSO-3DSA7T-32.786			
	Input Voltage	Operating Temp	Symmetry (Duty Cycle)	Output	Stability (in ppm)	Enable / Disable	Frequency
QSO	5 = 5.0V	D = -40° ~ +85°C	S = 40/60 max	A = CMOS	7 = ±20	T = Tristate	32.768KHz
	3 = 3.3V						
	2 = 2.5V						
	1 = 1.8V						

NOTE: A bypass capacitor is recommended between Vcc (pin 4) and Gnd (pin 2) to minimize power supply noise



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(Specifications subject to change without notice - Rev A)