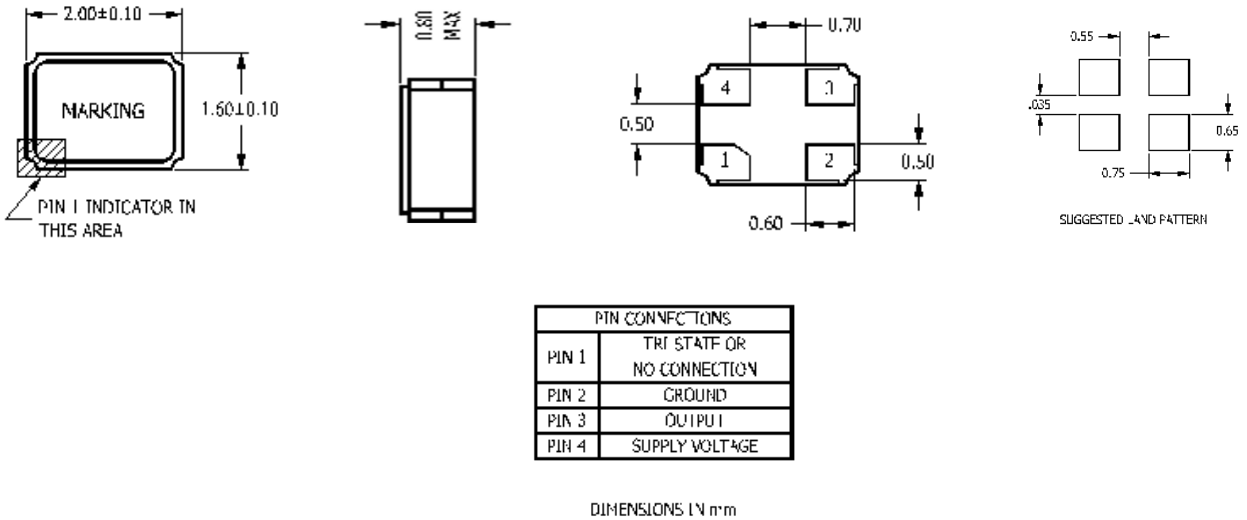
QYO Series

SMD Oscillator, 2x1.6mm, CMOS

Frequency	1.000 MHz to 80.000 MHz
Frequency Stability (Note 1)	See Part Number Guide
Supply Voltage ±5% (Vcc)	See Part Number Guide
Supply Current	4.0 mA
Output Levels Logic "0" Logic "1"	CMOS Less than 10% of Supply Voltage Greater than 90% of Supply Voltage
Symmetry (50% of waveform)	See Part Number Guide
Rise / Fall Time (20% to 80%)	6 nSec max
Output Load	See Part Number Guide
Load	15pF
Start Up Time	10 mSec max
Temperature	
Operating	See Part Number Guide
Storage	-55° C to + 125° C Standard
Tri-state Function (H)	Voh = 70% of Vdd min or No Connection to Enable Output Vol = 30% of Vdd max or grounded to Disable Output (High Impedance)
Notes:	1. Inclusive of Temperature Range, Load, Voltage and Aging. 2. A 0.01 uF bypass capacitor is recommend between VCC (Pin 4) and GND (Pin 2) to minimize power supply noise.

Part Number Guide **Sample Part Number: QYO-3ETA2T-40.000**

Package	Input Voltage	Operating Temperature	Symmetry (Duty Cycle)	Output	Stability (in ppm)	Pin 1 Select	Frequency
	1 = +1.80	A = 0ºC to +70ºC	T = 45/55	A = 15 pF	1 = ±100	H = Enable	
QYO	2 = +2.50	C = -20ºC to +70ºC	S = 40/60		2 = ±50	0 = N/C	40.000MHz
	3 = +3.30	E = -40ºC to +85ºC			3 = ±25		
		G = -40°C to 125°C			4 = ±20		



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